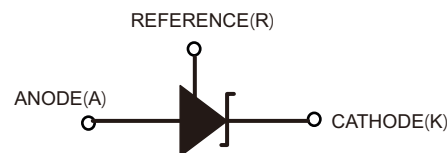


## DESCRIPTION

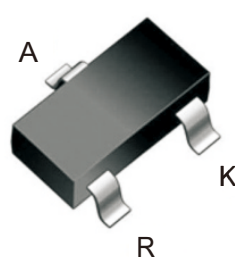
The JD431/JD431SDLWD are three-terminal adjustable shunt regulators with specified thermal stability. The output voltage may be set to any value between  $V_{ref}$  and 36V with two external resistors. Active output circuitry provides a very sharp turnon characteristic, making these devices excellent replacements for zener diodes in many applications.

## SOT-23

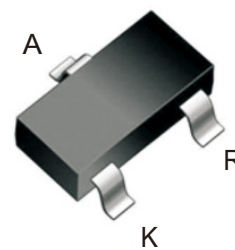


## Features

- The output voltage can be adjusted 2.495V to 36V
- The JD431/JD431SDLWD precision reference is offered in two voltage tolerance: 0.3%.
- Fast turn-on response
- Sink current capability 1mA to 100mA
- Low output noise
- Industrial temperature range
- Totally Lead-Free & Fully RoHS Compliant(Note 1)
- Halogen and Antimony Free. "Green" Device (Note2)



**JD431DLWD**



**JD431SDLWD**

## Application

- Shunt regulator
- High-current shunt regulator
- Precision current limiter

## Absolute Maximum Ratings (Note 3)

Symbol	Parameter		Rating	Unit
$V_{KA}$	Cathode Voltage		36	V
$I_{KA}$	Cathode Current Range (Continuous)		-100 to 150	mA
$I_{REF}$	Reference Input Current Range		10	mA
$P_D$	Power Dissipation		370	mW
$\theta_{JA}$	Thermal Resistance (Junction to Ambient)	SOT-23	380	°C/W
$T_J$	Junction Temperature		+150	°C
$T_{STG}$	Storage Temperature Range		-65 to +150	°C
ESD	ESD (Human Body Model)		2000	V

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. Halogen-and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br +Cl) and <1000ppm antimony compounds.
  3. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.



## Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
$V_{KA}$	Cathode Voltage	$V_{REF}$	36	V
$I_{KA}$	Cathode Current	1.0	100	mA
$T_A$	Operating Ambient Temperature Range	-40	+125	°C

## Electrical Characteristics (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Symbol	Parameter		Conditions	Min	Typ	Max	Unit	
$V_{REF}$	Reference Voltage	0.3%	JD431DLWD JD431SDLWD	$V_{KA} = V_{REF}, I_{KA} = 10\text{mA}$	2.488	2.495	2.502	V
$\Delta V_{REF}$	Deviation of Reference Voltage Over Full Temperature Range		$V_{KA} = V_{REF}, I_{KA} = 10\text{mA}$ $T_{min} \leq T_A \leq T_{max}$	—	14	34	mV	
$\frac{\Delta V_{REF}}{\Delta V_{KA}}$	Ratio of Change in Reference Voltage to the Change in Cathode Voltage		$I_{KA} = 10\text{mA}$	—	-1.0	-2.7	mV/V	
			$\Delta V_{KA} = 10\text{V to } V_{REF}$ $\Delta V_{KA} = 36\text{V to } 10\text{V}$	—	-0.5	-2.0		
$I_{REF}$	Reference Current		$I_{KA} = 10\text{mA}, R_1 = 10\text{k}\Omega,$ $R_2 = \infty$	—	1.5	4	$\mu\text{A}$	
$\Delta I_{REF}$	Deviation of Reference Current Over Full Temperature Range		$I_{KA} = 10\text{mA}, R_1 = 10\text{k}\Omega$ $R_2 = \infty, T_A = \text{full Temperature}$	—	0.4	1.2	$\mu\text{A}$	
$I_{KA}(\text{Min})$	Minimum Cathode Current for Regulation		$V_{KA} = V_{REF}$	—	0.45	1.0	mA	
$I_{KA}(\text{Off})$	Off-state Cathode Current		$V_{KA} = 36\text{V}, V_{REF} = 0$	—	0.05	1.0	$\mu\text{A}$	
$Z_{KA}$	Dynamic Impedance		$V_{KA} = V_{REF}, I_{KA} = 1 \text{ to } 100\text{mA},$ $f \leq 1.0 \text{ kHz}$	—	0.3	0.5	$\Omega$	



FIGURE 1. TEST CIRCUIT FOR  $V_{KA} = V_{REF}$

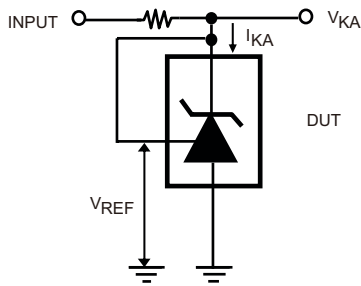


FIGURE 2. TEST CIRCUIT FOR  $V_{KA} \geq V_{REF}$

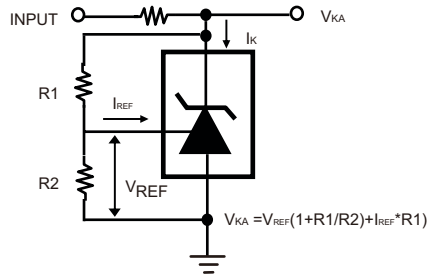


FIGURE 3. TEST CIRCUIT FOR  $I_{KA}$  (OFF)

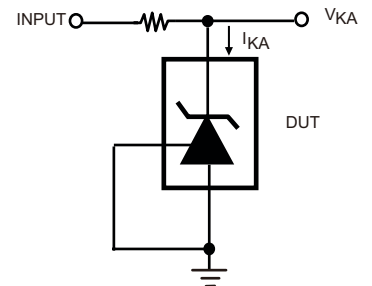


FIGURE 4. TEST CIRCUIT FOR PULSE RESPONSE

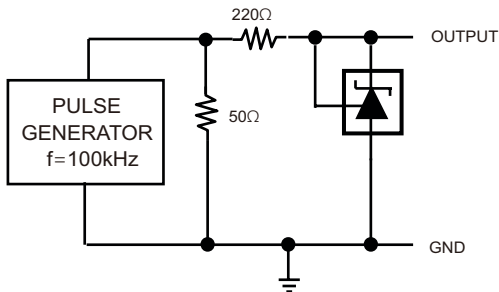
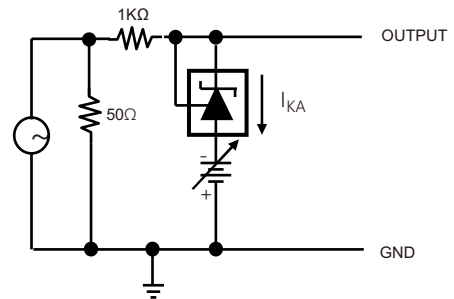


FIGURE 5. TEST CIRCUIT REFERENCE IMPEDANCE





### Typical Characteristics

Fig.1 CATHODE CURRENT VS CATHODE VOLTAGE

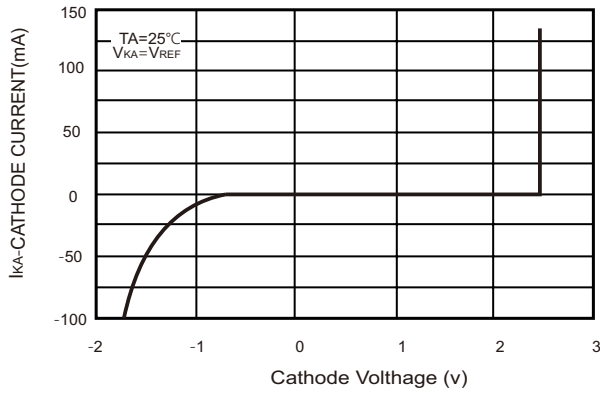


Fig.2 CATHODE CURRENT VS CATHODE VOLTAGE

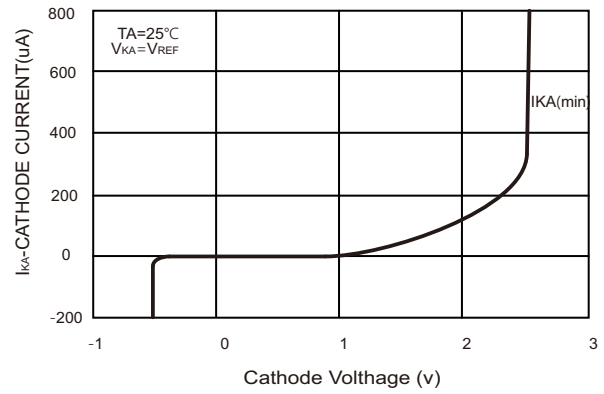


Fig.3 CHANGE IN REFERENCE INPUT VOLTAGE VS CATHODE VOLTAGE

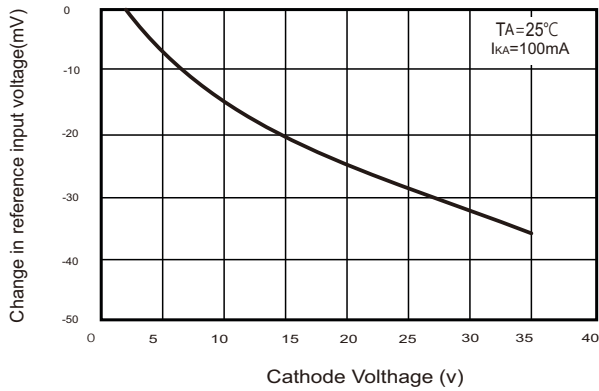


Fig.4 PULSE RESPONSE

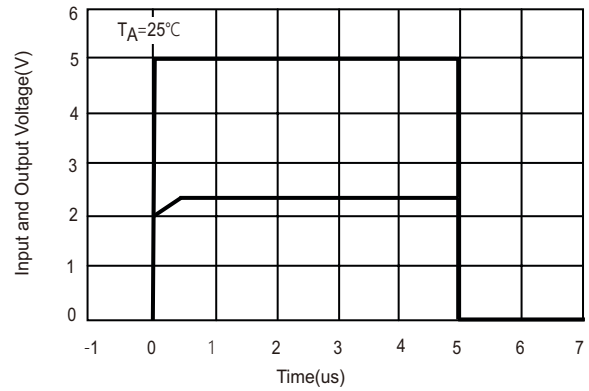


Fig.5 IMPEDANCE VS FREQUENCY

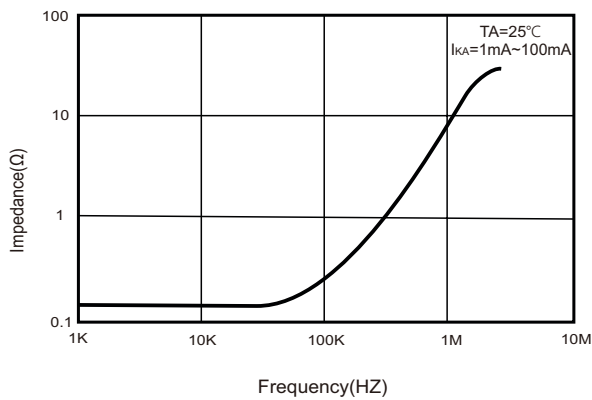
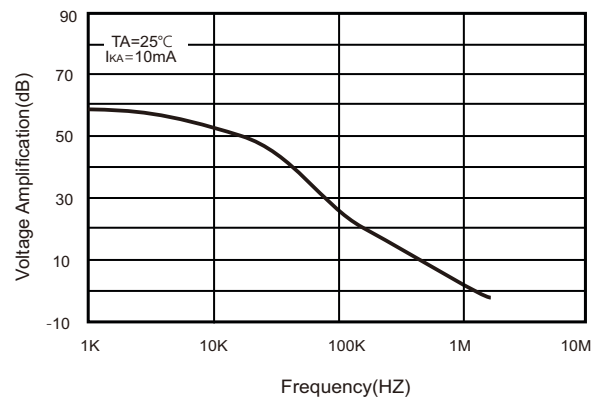
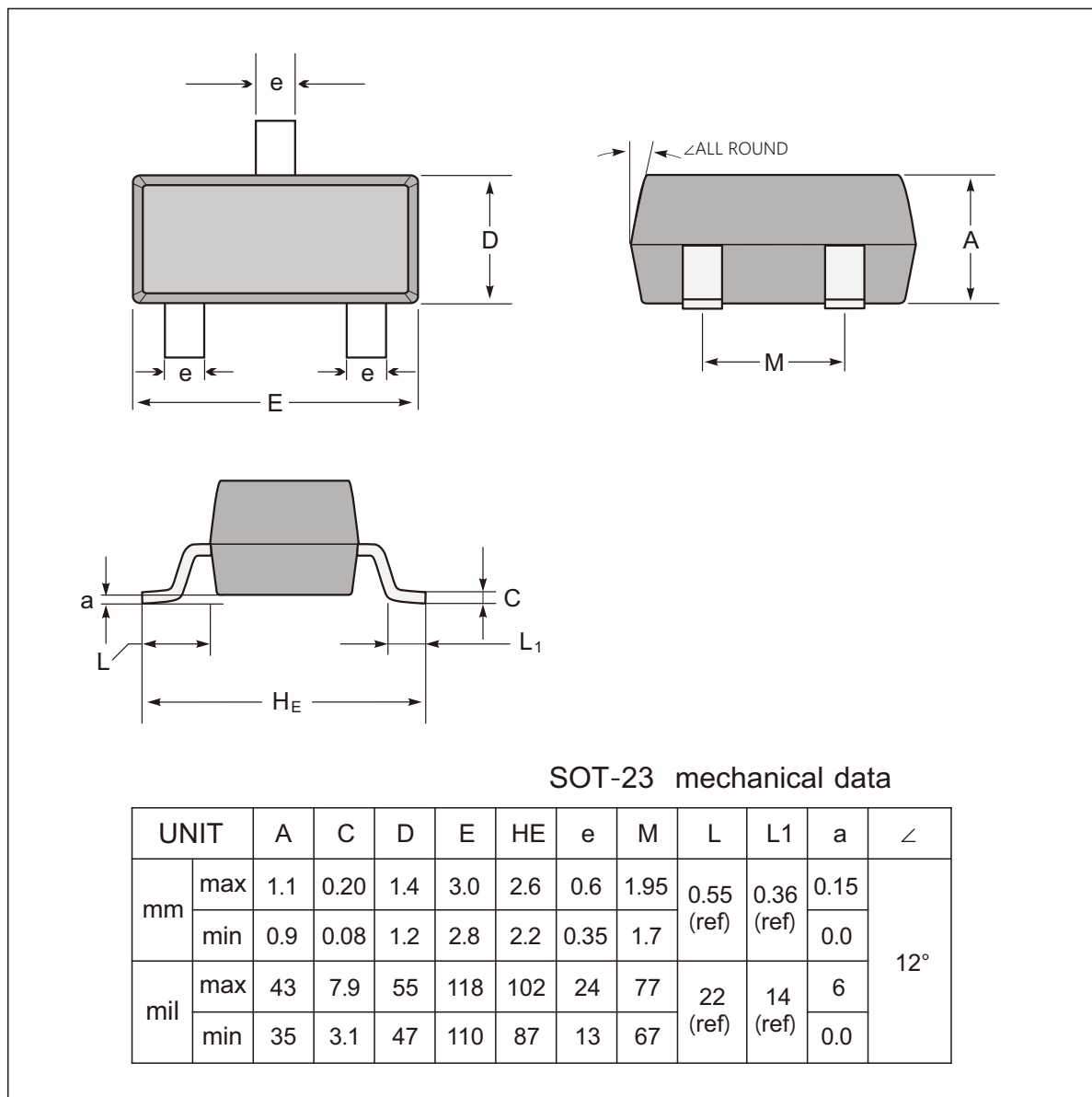


Fig.6 SMALL SIGNAL VOLTAGE AMPLIFICATION VS FREQUENCY

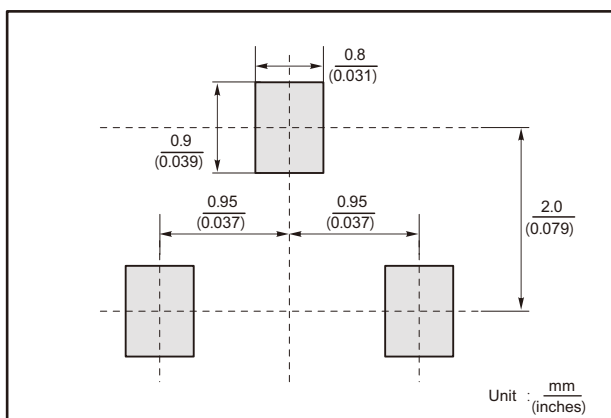




### SOT-23 Package Outline Dimensions



#### The recommended mounting pad size



#### Marking

NumbType	Marking code
JD431DLWD	J431DL
JD431SDLWD	431JDL